Makenzie Brian

ECE 474

Homework 4

4.

1. Total area used by alu: 1475.871283
2. Number of different types of gate utilized: 21
3. Area of NAND2X1 in micrometers squared: 5.5296

Gate equivalent count: 266.903805

1. This block is alu\_DW01\_addsub\_0. It is the addition and subtraction block. It is implemented with a full adder and two XOR gates.
2. Max delay path through alu: 2.51

Beginning point: opcode [0]

End point: alu\_zero

I did not encounter a lot of issues with this homework.